Abstract Of The Disclosure
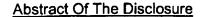
A computer system according to the present invention comprises at least two CPUs; at least one shared system resource accessible to

5   each of the CPUs; and shared memory shared by the CPUs. The shared memory has therein a resource locking table, comprising memory elements. Each of the memory elements is designated for being written to by only one of the at least two CPUs. Each of the at least two CPUs is communicatively interconnected with the

10   shared memory and the shared system resource, and the resource locking table is operative by each of the CPUs.